

ABSTRACT OF THE DISCLOSURE

To a level shift basic circuit having a CMOS configuration and composed of four transistors **M1** through **M4**, a control circuit for preventing feed-through current through the transistors is added. Transitions of complementary data inputs **Vin1** and **Vin2** are
5 made in a period in which n-MOS transistors **M7** and **M8** for control are turned OFF by changing a control input **VS1** to an L level (switch-off period). In this switch-off period, each source of the n-MOS transistors **M1** and **M2** is disconnected from VSS. In addition, in the switch-off period, a control input **VS2** is changed to an L level, thereby turning ON p-MOS transistors **M5** and **M6** for control. In a period in which the control p-MOS
10 transistors **M5** and **M6** are ON, data outputs **Vout1** and **Vout2** are both precharged to VDD (precharge period).